

U.S. Department of Commerce, Patent and Trademark Office				Atty Docket No.		Serial No.		
				M-1007-6C US		08/632,298		
INFORMATION DISCLOSURE STATEMENT BY APPLICANT (Use several sheets if necessary)				Applicant				
				Amr M. Mohsen				
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				April 12, 1996		2304 2763		
U.S. Patent Documents								
*Examiner Initial		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate	
	AA							
	AB							
	AC							
	AD							
	AE							
	AF							
	AG							
	AH							
	AI							
	AJ							
	AK							
Foreign Patent Documents								
							Translation	
		Document	Date	Country	Class	Subclass	Yes	No
	AL	0 335 281 A	10/4/89	Europe			<input type="checkbox"/>	<input checked="" type="checkbox"/>
	AM	0 481 703 A	4/22/92	Europe			<input type="checkbox"/>	<input type="checkbox"/>
	AN	0 518 701 A	12/16/92	Europe			<input type="checkbox"/>	<input type="checkbox"/>
	AO							
	AP							
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)								
	AR	Guo et al, "A 1024 Pin Universal Interconnect Array With Routing Architecture", <u>Proceedings of the Custom Integrated Circuits Conference</u> , May 3-6, 1992, pages 4.5.1 - 4.5.4.						
	AS							
	AT							
Examiner	V. TRANS	Date Considered						6/19/99
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